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MODEL 2322 DUAL 24-BIT PARALLEL INPUT REGISTER

FEATURES

- . Dual 24-Bit Data Input Register
- . External Strobe Latches Data and Set LAM
- . Data Accepted returned to external device

APPLICATIONS

- . General Purpose Data Acquisition
- . Monitor TTL Signals
- . Monitor Contact Closures
- . Input Module For Nuclear ADC

General Description

The Model 2322 is a single-width module that provides Dual 24-bit registers for holding binary input data. Each register can be loaded from an external device and read by the dataway R1-R24. An external LOAD DATA strobe latches the input data in the individual registers and sets separate LAM's plus after the leading edge of the strobe. A .05us DATA ACCEPTED signal is returned at the time data is stored in the register. Load Data and Data Accepted signals are strap selectable for active-going High or Low signals. The Model 2322 can be strapped to load the registers with the read commands.

Interrupt Capabilities

Each register has a LAM status flip-flop and a MASK flip-flop. The Load Data strobe gets the status register and if LAM Enable is set, a LAM request is initiated. The source of the LAM can be tested with the TTL command. If an external strobe is received with a LAM present, the data will not be strobed into the register until the LAM is cleared by either reading the register or clearing the LAM. When the LAM is cleared, the data will be loaded into the register and a new LAM request initiated.

INPUTS

All external connections for each register are via front panel mounted 2DB52P connectors. All inputs (data and strobe) are low-true with a TRUE input being represented by an impedance to ground of less than 200 ohms. Ground connected relay contacts, TTL outputs, and common emitter transistor circuits are satisfactory sources of data. The inputs are diode protected. High true inputs are optional and DD50P connectors are optional.

