

# MODEL 6701



## CRATE VERIFIER

### FEATURES

- ◆ Monitor CAMAC Addresses and Functions for Every CAMAC Cycle (S2).
- ◆ Rotate "Walking-One" Pattern through All Zeros on the 24 Bits of Dataway.
- ◆ Rotate "walking-Zero" Pattern through All Ones On the 24 Bits of Dataway.
- ◆ Store Data On the W-Lines.
- ◆ Read Crate Voltages and Temperature.

### DESCRIPTION

The DSP Technology Model 6701 Crate Verifier allows monitoring the status of the CAMAC Crate in a totally programmable fashion by the host CPU.

Supply voltages, individual traces on the DATAWAY, crate ambient temperature, and present status of CAMAC commands are stored in the module for computer readout.

Multicrate systems need this type of continuous monitoring to assure the data acquisition system is not outside of CAMAC specifications. Modules can be damaged by gross variations in temperature or supply voltages.

### TECHNICAL SPECIFICATIONS

#### PACKAGING

# 1 Width CAMAC Module.  
221 mm H, 18 mm W, 292 mm D\* (8.7", x 0.7" x 11.5")  
\*Depth front to rear panel. Rear connector 13 mm (0.5").

In conformance with the CAMAC standard for RF shielded instrumentation modules (IEEE standard 583, European Esone Report #EUR4100e).

#### COMPUTER COMMANDS

- Z+ C: Clear LAM and clear ROTATE register.  
F(20)A(C): Load DATA register.  
F(20)A(1): Set LAM.  
F(20)A(2): Reset LAM.  
F(20)A(3): Set ROTATE register for walking zero.  
F(3): Read COMMAND register  
F(4)A(0): Read DATA register.  
F(4)A(1): Read ROTATE register and rotate pattern left.  
F(4)A(2): Read ROTATE register and don't rotate.  
F(4)A(3): Read identification switches  
F(5)A(n): Read analog voltages.

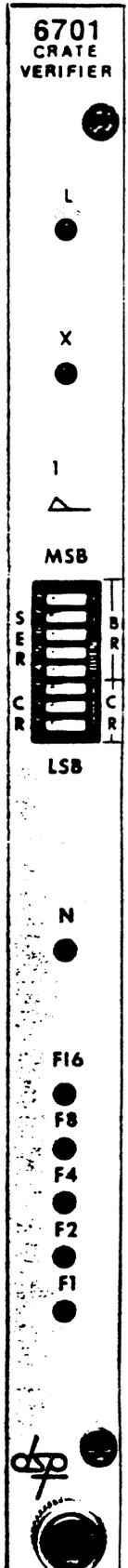
#### ANALOG VOLTAGE TABLE:

A) Read analog voltages "n" on R8..R1 with F(5)A(n).

$$V \text{ (mv)} = f(40.V_{in} - 5120)$$

n	SOURCE	f
0	+ 24	6
1	+ 12	3
2	+ 6	1.5
3	ground	1.0
4	- 6	1.5
5	- 12	3
6	- 24	6
7	temperature	1
8	P1	6
9	P2	6
10	P3	6
11	P4	6
12	P5	6

B) Temperature = 40 mv per °C,  
°C = 0 V.



ACTUAL SIZE