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# Bi Ra Systems, Inc.

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3520-D PAN AMERICAN NORTHEAST • ALBUQUERQUE, NEW MEXICO 87107 • (505) 345 - 5864

Model 2352

## DISCRIMINATOR COINCIDENCE REGISTER

The Model 2352 is a double width module that contains a 16 bit register with data inputs via front panel Lemo connectors. The data inputs are adjustable in threshold from 0 to -600 mv. The data will be entered into the register if an overlap of  $\geq 1$  nsec is found with the Gate input. The Gate input sets a LAM register that inhibits further inputs until reset. A front panel Lemo is provided for a Clear signal ( $\geq 2$  nsec) that will reset all data registers and the LAM. A front panel Test Point and potentiometer are provided for threshold adjustments. Indicators are provided for N, I, LAM Enable, and LAM. Patch pins are provided internally for selection of Inhibit mode. Data taking can be Inhibited from Dataway I or from LAM Enable or from both or neither.

The Model 2352 has the following commands:

<u>COMMAND</u>	<u>ACTION</u>
F(0)·A(0)	Read the data register
F(2)·A(0)	Read the data register and reset Data and LAM
F(9)·A(0)	Clear Data and LAM register
F(24)·A(0)	Reset LAM Enable
F(25)·A(0)	Set all Data registers and LAM on S2
F(26)·A(0)	Set LAM Enable on S2
F(27)·A(0)	Test LAM. Q=1 if LAM set

All commands return X and Q except F(27)·A(0) which returns X and a conditional Q.

Z·S2 and C·S2	Resets Data and LAM registers
I	Inhibits Data and LAM registers from being set if patched in

All Lemo Inputs are terminated in 50 $\Omega$