

JOERGER ENTERPRISES, INC.

QUAD 24 BIT OUTPUT REGISTER, MODEL QOR

FEATURES:

HIGH PACKAGING DENSITY
HANDSHAKE LOGIC FOR DATA TRANSFER
PROGRAMMABLE POLARITY SELECTION

THE MODEL QOR IS A FOUR CHANNEL OUTPUT REGISTER PACKAGED IN A SINGLE WIDTH CAMAC MODULE. EACH CHANNEL CONTAINS 24 BITS. ERROR FREE DATA TRANSFER IS PROVIDED BY THE USE OF A HANDSHAKE MODE IN CONJUNCTION WITH THE INTERRUPT LINE L. HANDSHAKE IS ACCOMPLISHED BY THE USE OF A DATA READY OUTPUT SIGNAL AND A BUSY INPUT SIGNAL. WHILE THE BUSY LINE IS DOWN, THAT CHANNEL WILL NOT BE ABLE TO UPDATE AND SO OUTPUT DATA STABILITY IS INSURED. IF AN UPDATE COMMAND IS RECEIVED AT THIS TIME, IT WILL RETURN A Q=0 RESPONSE. WHEN THE BUSY LINE COMES UP, INDICATING THE OUTPUT DEVICE IS FINISHED WITH THE DATA, THE LAM FLIP-FLOP IS SET. IF THE UNIT IS ENABLED, IT WILL GENERATE AN L SIGNAL WHICH SIGNALS THE CONTROLLER THAT NEW DATA CAN BE ACCEPTED. AN UPDATE PULSE IS ALSO GENERATED TO INDICATE WHEN A CHANNEL HAS BEEN OVERWRITTEN, AND CAN BE USED TO TRIGGER AN EXTERNAL DEVICE WITHOUT THE USE OF THE HANDSHAKE SIGNALS. OUTPUT POLARITY SELECTION IS AVAILABLE AS AN OPTION. THIS ALLOWS THE POLARITY OF THE DATA TO BE INVERTED BY THE USE OF SOLDER LINKS OR UNDER PROGRAM CONTROL. THIS IS ACCOMPLISHED BY ALLOWING BIT 24 (OR 16) TO SELECT THE POLARITY OF THE OUTPUT DATA FOR THAT CHANNEL.

QUAD
OUTPUT
REGISTER
MODEL QOR

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SPECIFICATIONS

OUTPUTS (EACH CHANNEL)

24 DATA BITS	LOGIC "0"	+2.4v
	LOGIC "1"	+0.5v SINKING 25MA
DATA READY	USED FOR DATA TRANSFER. IT'S LEADING EDGE INDICATES DATA HAS BEEN LOADED INTO THE REGISTER.	
UPDATE	A PULSE WHICH INDICATES THE CHANNEL HAS BEEN UPDATED.	

INPUTS (EACH CHANNEL)

BUSY	HANDSHAKE RESPONSE, LEADING EDGE RESETS DATA READY SIGNAL AND TRAILING EDGE, WHICH INDICATES DATA HAS BEEN ACCEPTED BY OUTPUT DEVICE, SETS LAM TO INDICATE A NEW TRANSFER CAN BE MADE.
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CAMAC COMMANDS

N.F8.A0	TEST LAM FLIP-FLOP FOR CHANNEL 0, GENERATES Q RESPONSE IF LAM IS SET AND CHANNEL IS ENABLED.
N.F8.A1-3	SAME AS ABOVE FOR CHANNELS 1, 2, AND 3.