SECTION I

GENERAL INFORMATION

1.1 INTRODUCTION

This manual describes the elements of operation and installation procedures of the **MM6346D** Dual Port Dynamic Random Access Memory Module.

1.2 GENERAL DESCRIPTION

The **MM6346D** Dual Ported Memory is compatible with the **VMEbus, VME Subsystem Bus (VSBus)** specifications. Inherent to the module are **VMEbus** options D32, D16 & D8 (32, 16 & 8 bit data path width), and A32, A24 (32, 24 bit address path width). The module also generates and stores an even parity bit for each byte written on write cycles and checks parity for each byte read on read cycles. Then if a parity error is detected, the module sets a **Control Status Register** (CSR) bit and may be programmed to assert the bus error signal on the requesting port. (see Section III for CSR programming)

For Extremely High Performance operation, the **MM6346D** Dual Ported Memory is equipped with a Dual Port PAGE MODE High Speed ***BLOCK Transfer*** capability. This allows the use of High Performance Block Mode Controllers to transfer upto 256 bytes on the **VMEbus** and **VSBus**, for increased system performance.

While the **MM6346D** Dual Ported Memory was designed as a 32 bit wide (data path) memory board, and complies with **VMEbus** Specifications (Revision C.1) and **VSBus** Specifications (Revision C) and can be addressed as 8 bit bytes, 16 bit words, or 32 bit longwords. The **MM6346D** Dual Ported Memory can be configured from 16M (64M) to 128M (512M) bytes capacity by populating it with 4096K X 9 (16384K X 9) 80ns CMOS DRAM Single-Inline-Packages (SIP).

The **MM6346D** Dual Ported Memory memory modules are available in several options depending on the capacity required. Table 1.1 lists the optional part numbers for ordering purposes, the total memory capacity provided by each version.

Table 1.1 Model Numbering

Model Number	Capacity
MM6346D/16M	16.0M Bytes
MM6346D/32M	32.0M Bytes
MM6346D/64M	64.0M Bytes
MM6346D/128M	128.0M Bytes
MM6346D/256M	256.0M Bytes
MM6346D/512M	512.0M Bytes